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HARLEY COLLINS

Real-Time Systems Design and Analysis Morgan Kaufmann
This book constitutes the proceedings of the SPEC Benchmark Workshop 2009 held in Austin, Texas, USA on January 25th, 2009. The 9 papers presented were carefully selected and reviewed for inclusion in the book. The result is a collection of high-quality papers discussing current issues in the area of benchmarking research and technology. The topics covered are: benchmark suites, CPU benchmarking, power/thermal benchmarking, and modeling and sampling techniques.

Image and Signal Processing Springer

"This comprehensive collection of papers offers you practical information that can be used to develop high-performance digital system design. Specially written introductions by editor Vojin G. Oklobdzija precede each chapter to aid your understanding of the most relevant topics in this advanced area of circuit design. Featured topics include: * Differential pass-transistor logic * High-speed circuits and design of high-performance systems * Advanced deep submicron circuits used in high-speed computers and digital circuits * Clocking and latch design essential to high-performance systems * Relationships between VLSI algorithms and implementation techniques HIGH PERFORMANCE SYSTEM DESIGN: Circuits and Logic is indispensable reading for circuit designers, practicing engineers, and students who want to master the basic principles underlying high-performance system design. This handy, single volume provides a useful reference to a collection of accumulated experience necessary for good, successful designs. Professors: To request an examination copy simply e-mail collegeadoption@ieee.org." Sponsored by: IEEE Solid-State Circuits Council/Society.

2020 IEEE 5th International Conference on Computing Communication and Automation (ICCCA) John Wiley & Sons
Author Impact

The RISC-V Reader Pearson Education India

Reduced Instruction Set Computers (RISC) reduce the number of instructions performed by the microprocessor. This volume provides an overview of RISC as both a design philosophy and a marketing and technical force. It introduces the fundamentals of RISC mic

2017 International Conference on Nextgen Electronic Technologies Silicon to Software (ICNETS2) Prentice Hall
Conceptual and precise, Modern Processor Design brings together numerous microarchitectural techniques in a clear, understandable framework that is easily accessible to both graduate and undergraduate students. Complex practices are distilled into foundational principles to reveal the authors insights and hands-on experience in the effective design of contemporary high-performance micro-processors for mobile, desktop, and server markets. Key theoretical and foundational principles are presented in a systematic way to ensure comprehension of important implementation issues. The text presents fundamental

concepts and foundational techniques such as processor design, pipelined processors, memory and I/O systems, and especially superscalar organization and implementations. Two case studies and an extensive survey of actual commercial superscalar processors reveal real-world developments in processor design and performance. A thorough overview of advanced instruction flow techniques, including developments in advanced branch predictors, is incorporated. Each chapter concludes with homework problems that will institute the groundwork for emerging techniques in the field and an introduction to multiprocessor systems.

2021 8th International Conference on Signal Processing and Integrated Networks (SPIN) Springer Science & Business Media
This new volume introduces various VLSI (very-large-scale integration) architecture for DSP filters, speech filters, and image filters, detailing their key applications and discussing different aspects and technologies used in VLSI design, models and architectures, and more. The volume explores the major challenges with the aim to develop real-time hardware architecture designs that are compact and accurate. It provides useful research in the field of computer arithmetic and can be applied for various arithmetic circuits, for their digital implementation schemes, and for performance considerations. Principles of Secure Processor Architecture Design Wiley-IEEE Press

The Conference aims to provide a forum for electrical engineers and scientists in academia and industry, to present their works and to share their experiences in the area of electrical and electronics engineering

The SPARC Architecture Manual Waveland Press

To bring together researchers and faculty members interested in Circuit theory, Electromagnetic field theory, Circuit analysis and synthesis, Electromagnetic field analysis, Circuit design, Inverse problems, Technical magnetism, Coupled problems, Electrical machines and drives, Electromagnetic compatibility, Sensors and transducers, Power electronics, Power engineering, Bioengineering and other interdisciplinary areas

2018 International Symposium on Fundamentals of Electrical Engineering (ISFEE) Springer Science & Business Media
Computing, renewable, communication, automation & Robotics
Computer Organization and Design Springer

Provides the only up-to-date source on the most recent advances in this often complex and fascinating topic. The only book to be entirely devoted to clocking Clocking has become one of the most important topics in the field of digital system design A "must have" book for advanced circuit engineers

Computer Architecture Springer Nature

In DSP Architecture Design Essentials, authors Dejan Marković and Robert W. Brodersen cover a key subject for the successful realization of DSP algorithms for communications, multimedia, and healthcare applications. The book addresses the need for DSP architecture design that maps advanced DSP algorithms to hardware in the most power- and area-efficient way. The key feature of this text is a design methodology based on a high-level

design model that leads to hardware implementation with minimum power and area. The methodology includes algorithm-level considerations such as automated word-length reduction and intrinsic data properties that can be leveraged to reduce hardware complexity. From a high-level data-flow graph model, an architecture exploration methodology based on linear programming is used to create an array of architectural solutions tailored to the underlying hardware technology. The book is supplemented with online material: bibliography, design examples, CAD tutorials and custom software.

2019 5th International Conference on Advanced Computing and Communication Systems (ICACCS) CRC Press

This book constitutes the refereed proceedings of the 12th International Conference on Field-Programmable Logic and Applications, FPL 2002, held in Montpellier, France, in September 2002. The 104 revised regular papers and 27 poster papers presented together with three invited contributions were carefully reviewed and selected from 214 submissions. The papers are organized in topical sections on rapid prototyping, FPGA synthesis, custom computing engines, DSP applications, reconfigurable fabrics, dynamic reconfiguration, routing and placement, power estimation, synthesis issues, communication applications, new technologies, reconfigurable architectures, multimedia applications, FPGA-based arithmetic, reconfigurable processors, testing and fault-tolerance, crypto applications, multitasking, compilation techniques, etc.

Computer Architecture Springer

Acknowledgments. Basic Real-Time Concepts. Computer Hardware. Languages Issues. The Software Life Cycle. Real-Time Specification and Design Techniques. Real-Time Kernels. Intertask Communication and Synchronization. Real-Time Memory Management. System Performance Analysis and Optimization. Queuing Models. Reliability, Testing, and Fault Tolerance. Multiprocessing Systems. Hardware/Software Integration. Real-Time Applications. Glossary. Bibliography. Index.

2019 1st International Informatics and Software Engineering Conference (UBMYK) Wiley-IEEE Press

2021 International Conference on Advanced Computing and Communication Systems (ICACCS) aims at exploring the interface between the industry and real time environment with state of the art techniques ICACCS 2021 publishes original and timely research papers and survey articles in current areas of energy, smart city, temperature, power and environment related research areas of current importance to readers

2021 IEEE International Conference on Computer Science, Electronic Information Engineering and Intelligent Control Technology (CEI) Springer Science & Business Media

This volume is the proceedings of the 13th International Conference on Theorem Proving in Higher Order Logics (TPHOLS 2000) held 14-18 August 2000 in Portland, Oregon, USA. Each of the 55 papers submitted in the full research category was refereed by at least three reviewers who were selected by the program committee. Because of the limited space available in the program and proceedings, only 29 papers were accepted for presentation and publication in this volume. In keeping with tradition, TPHOLS 2000 also offered a venue for the presentation of work in progress, where researchers invite discussion by means of a brief preliminary talk and then discuss their work at a poster session. A supplementary proceedings containing associated papers for work in progress was published by the Oregon Graduate Institute (OGI) as technical report CSE-00-009. The organizers are grateful to Bob Colwell, Robin Milner and Larry Wos for agreeing to give invited talks. Bob Colwell was the lead architect on the Intel P6 microarchitecture, which introduced a number of innovative techniques and achieved enormous

commercial success. As such, he is ideally placed to offer an industrial perspective on the challenges for formal verification. Robin Milner contributed many key ideas to computer theorem proving, and to functional programming, through his leadership of the influential Edinburgh LCF project.

A Practitioner's Guide to RISC Microprocessor Architecture Springer

This in-depth guide to Version 8 SPARC, a high-speed RISC computer chip, provides the reader with the background, design philosophy, high-level features and implementations of this new model. Includes an expanded index of terms for easy reference and a table of synthetic instructions added to the suggested assembly language syntax.

Modern Processor Design Self

With growing interest in computer security and the protection of the code and data which execute on commodity computers, the amount of hardware security features in today's processors has increased significantly over the recent years. No longer of just academic interest, security features inside processors have been embraced by industry as well, with a number of commercial secure processor architectures available today. This book aims to give readers insights into the principles behind the design of academic and commercial secure processor architectures. Secure processor architecture research is concerned with exploring and designing hardware features inside computer processors, features which can help protect confidentiality and integrity of the code and data executing on the processor. Unlike traditional processor architecture research that focuses on performance, efficiency, and energy as the first-order design objectives, secure processor architecture design has security as the first-order design objective (while still keeping the others as important design aspects that need to be considered). This book aims to present the different challenges of secure processor architecture design to graduate students interested in research on architecture and hardware security and computer architects working in industry interested in adding security features to their designs. It aims to educate readers about how the different challenges have been solved in the past and what are the best practices, i.e., the principles, for design of new secure processor architectures. Based on the careful review of past work by many computer architects and security researchers, readers also will come to know the five basic principles needed for secure processor architecture design. The book also presents existing research challenges and potential new research directions.

Finally, this book presents numerous design suggestions, as well as discusses pitfalls and fallacies that designers should avoid.

2020 24th International Symposium on VLSI Design and Test (VDAT). Springer Science & Business Media

The research domains like Computing, Communication, Control and Automation has led to exponential increase in the number of people using these technologies and also their interest in research and development activities To prepare ourselves for this global competition, Pimpri Chinchwad College of Engineering, Pune has conceptualized the 4th International Conference on Computing Communication Control and Automation (ICCUBEA) 2018 under IEEE Pune Section during 16th to 18th August, 2018 This three days International Conference ICCUBEA 2018 will focus on the latest research trends and applications in the domains of Computing, Communication, Control and Automation This conference is designed to provide a common platform to the academicians, research scholars, industry experts and students to spread knowledge on scientific research in Interdisciplinary areas Also the pre conference tutorials by the esteemed experts will enrich the technical takeaways for the delegates

2021 7th International Conference on Advanced

Computing and Communication Systems (ICACCS) Springer Science & Business Media

As the costs of power and timing become increasingly difficult to manage in traditional synchronous systems, designers are being forced to look at asynchronous alternatives. Based on reworked and expanded papers from the VII Banff Higher Order Workshop, this volume examines asynchronous methods which have been used in large circuit design, ranging from initial formal specification to more standard finite state machine based control models. Written by leading practitioners in the area, the papers cover many aspects of current practice including practical design, silicon compilation, and applications of formal specification. It

also includes a state-of-the-art survey of asynchronous hardware design. The resulting volume will be invaluable to anyone interested in designing correct asynchronous circuits which exhibit high performance or low power operation.

2021 International Conference on Emerging Smart Computing and Informatics (ESCI) Morgan Kaufmann

This excellent survey of state-of-the-art techniques discusses the MTCMOS technology that has emerged as an increasingly popular technique to control the escalating leakage power, while maintaining high performance. It addresses the leakage problem in a number of designs for combinational, sequential, dynamic and current-steering logic.